

Form PTO-1449 (Modified) LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	Atty Docket No. YAMAP0713US	Serial No. 09/580,223
	Applicant: Andrew Kay et al.	
	Filing Date May 26, 2000	Group 2123

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Sub-class	Filing Date if Appropriate

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FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Sub-class	Translation	
						Yes	No

OTHER ART

Examiner Initial	Author, Title, Date, Pertinent Pages, etc.
<i>ayel</i>	Ashenden et al. "Extensions to VHDL for Abstraction of Concurrency and Communication" July 19 1998, pages 301-308
<i>ayel</i>	Ashenden et al. "Considerations on system-level behavioural and structural modeling extensions to VHDL" March 16, 1998, pages 42-50
<i>ayel</i>	Sakurai et al. "A scheduling method for synchronous communication in the Bach hardware complier" January 18, 1999, pages 193-196
<i>ayel</i>	European Search Report regarding Application No. 00304528.3-2211 dated April 24, 2003

EXAMINER <i>ayel Sharon</i>	DATE CONSIDERED <i>11/14/03</i>
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Information Disclosure Statement PTO-1449 (Modified)

The identification of any reference is not intended to be, and should not be understood as being, an admission that such publication, in fact, constitutes "prior art" within the meaning of applicable law since, for example, a given reference may have a later effective date than first seems apparent or the reference may have an effective date which can be antedated. The "prior art" status of any reference is a matter to be resolved during prosecution.

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